

#5/Election  
Patent  
12/27/11

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Adkisson et al  
Serial No. 09/691,353  
Filed: October 18, 2000  
Title: METHOD OF FABRICATING  
SEMICONDUCTOR SIDE WALL FIN

Docket: BUR919990300US1  
Date: December 20, 2001  
Art Unit: 2823  
Examiner: Deven M. Collins

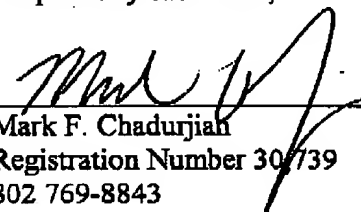
Response to Restriction Requirement

Commissioner of Patents & Trademarks  
Washington, D.C. 20231

Sir:

In response to the Restriction Requirement of November 30, 2001, the applicant hereby elects Group I, claims 1 and 14-23, without traverse.

Respectfully submitted,

  
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12/20/2001

IBM Intellectual Property Law 972E  
1000 River Street  
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## CERTIFICATE OF MAILING

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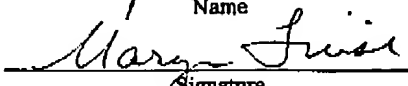
## MAIL

☐ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, DC 20231.

Date: 12/21/2001

## FACSIMILE

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Maryann Luisi  
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